

DIO2611 3-Vrms Audio Line Driver with Integrated One-ch, 6th-Order SD Video Filter

Features

- Line Output: able to drive 600Ω and 10kΩ
 -- 3Vrms With 5V Supply Voltage
 - No Pop/Clicks Noise at the line output when PowerON/OFF
 - No Need for Output DC-Blocking Capacitors
 - Accepting Single terminal Input
- SD Filter: One-ch 6th-order 11MHz SD filters
 - 6dB Output Driver Gain
 - Drive Dual Video Load
 - Transparent Input Clamping
 - AC or DC Coupled Inputs/Outputs
- Optimized Frequency Response between 20Hz–20kHz

Applications

- DVD Players
- Video Amplifiers
- Set-Top Boxes
- Personal Video Recorders
- Communications Devices

Ordering Information

Descriptions

The DIO2611is an integrated solution for Video driver and Audio driver, with two separated modules, the Audio Line Driver module and One-ch6th-order SD Video filter driver module.

The Audio Line Driver allow for the removal of output AC-coupling capacitors. It is designed to optimize the audio driver circuit performance while reducing the BOM cost by eliminating the peripheral discrete components for noise reduction. It is able to offer 3Vrms output with $2.5k\Omega$ load and 5V supply.

The 6th-order SD Video filter driver provides improved image quality compared with passive LC filters and discrete driver solution, especially suited for standard definition video signals. It can be directly driven by a DC-coupled DAC output or an AC-coupled signal. Internal diode-like clamps and bias circuitry may be used if AC-coupled inputs are required. The output can also drive AC or DC coupled single (150 Ω) or dual (75 Ω) loads. The DC coupling capacitors can be removed.

Order Part Number	Top Marking		T _A		Package
DIO2611CT16	DIO2611	Green/RoHS	-40 to +85°C	TSSOP-16	Tape & Reel, 2500



Pin Assignments



Figure 1 Pin Assignment

Pin Description

Pin Name	Description		
IN	Video filter driver Input		
VCC	Video filter driver power supply		
-INR	Right channel Negative Input		
OUTR	Right channel Output		
GND	Power ground		
EN	Mute input, active-low		
PVSS	Negative Supply Voltage		
CN	Charge-pump flying capacitor negative terminal		
CP	Charge-pump flying capacitor positive terminal		
PVDD	Audio line driver positive supply		
UVP	Under voltage protection input		
OUTL	Left channel Output		
-INL	Left channel Negative Input		
OUT	Video filter driver Output		



Block Diagram



Absolute Maximum Ratings

Stresses beyond those listed under "Absolute Maximum Rating" may cause permanent damage to the device. These are stress ratings only and functional operation of the device at these or any other condition beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maxim rating conditions for extended periods may affect device reliability.

Parameter		Rating	Unit
Supply Voltage/VCC		0 to 6.0	V
Supply Voltage/PVDD		-0.3 to 6.0	V
Input Voltage/VIN		-0.3 to V _{cc} +0.3	V
Input Voltage/-INR,-INL		V_{SS} -0.3 to V_{DD} +0.3	v
Minimum load impedance		600	Ω
EN to GND		-0.3 to V _{DD} +0.3	v
Storage Temperature Range		-65 to 150	°C
Junction Temperature		150	°C
Lead Temperature Range		260	°C
HBM ESD	Output Pins	8	
JEDEC: JESD22-A114	All Pins	5	κ.v.

www.dioo.com DIO2611• Rev.0.2



Recommended Operating Conditions

The Recommended Operating Conditions table defines the conditions for actual device operation to ensure optimal performance to the datasheet specifications. DIOO does not recommend exceeding them or designing to Absolute Maximum Ratings.

Symbol	Parameter	Min.	Тур.	Max.	Unit
PVDD	Supply Voltage	4.75	5	5.25	V
VCC	Supply Voltage	3.135		5.5	V
V _{IH}	EN High level Input Voltage	1.2			V
VIL	EN Low level Input Voltage			0.4	V
T _A	Operating Temperature Range	-40		85	°C

Notes: The items below were divided into two parts, the Audio Line driver module and the SD Video filter module.

Electrical Characteristics for the Audio Line Driver module

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
V _{OS}	Output Offset Voltage	V _{DD} =5V, Input grounded, Gain=100		1		mV
OVP	V _{DD} Over Voltage Protection	V_{DD} >5.5V, then IC shutdown	5.5			V
PSRR	Power supply rejection ratio	V _{DD} =5V		85		dB
V _{он}	High level output voltage	V_{DD} =5V, R _L =2.5k Ω	3.1			V
V _{OL}	Low level output voltage	V_{DD} =5V, R _L =2.5k Ω			-3.05	V
Цн	EN High level input current	V_{DD} =5V, V_{I} = V_{DD}			1	μA
IIL	EN Low level input current	V _{DD} =5V, V _I =0V			1	μA
	Supply current	V_{DD} =5V, V _I = V_{DD} , No load		14		mA
IDD		Shutdown mode, V _{DD} = 5V			1.5	
Vo	Output Voltage	THD=1%, V_{DD} =5V, f=1kHz, R _L =100k Ω	3.1			V_{RMS}
THD+N	Total harmonic distortion + noise	$V_0=2V_{RMS}$, f=1kHz, R _L =10k Ω		0.001		%
X _{TALK}	Channel crosstalk	V _O =2V _{RMS} , f=1kHz		-100		dB
lo	Maximum output current	V _{DD} =5V		18		mA
SNR	Signal noise ratio	V _O =3V _{RMS} , BW=22kHz, A-weighted		108		dB
SR	Slew rate			11.8		V/µs
CL	Maximum capacitive load			220		pF
V _N	Noise output voltage	BW=20Hz to 22kHz		6		μV_{RMS}
G _{BW}	Unity gain bandwidth			8		MHz
Avo	Open loop voltage gain			150		dB
V _{UVP}	External under-voltage detection			1.205		V
I _{Hys}	External under-voltage detection hysteresis current			5		μA

Typical value: $T_A = 25^{\circ}C$, $V_{DD}=5V$, unless otherwise specified.



Electrical Characteristics for the SD Video Filter module

Typical value: $T_A = 25^{\circ}C$, $V_{CC}=5V$, $R_{SOURCE}=37.5\Omega$, $R_L=150\Omega$ loads; all inputs are AC couple with 0.1μ F; all outputs are AC coupled with 220μ F; unless otherwise specified.

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit		
	DC ELECTRICAL CHARACTERISTICS							
I _{CC}	Supply Current			8	12	mA		
V _{IN}	Video Input Voltage Range		GND	1.4		V _{PP}		
PSRR	Power Supply Rejection			-50		dB		
DYNAMIC PE	RFORMANCE		·					
AV	Channel Gain		5.8	6.0	6.2	dB		
f _{1dB}	-1dB Bandwidth		8.0	10.5		MHz		
f _{3dB}	-3dB Bandwidth		9	11.8		MHz		
	Filter Response	f=27MHz		-40		dB		
DG	Differential Gain			0.2		%		
DP	Differential Phase			0.36		o		
THD	Output Distortion	f=1MHz		0.38		%		
X _{TALK}	Crosstalk	f=1MHz		-74		dB		
SNR	Signal to Noise Ratio			70		dB		
	Group Delay	f=400kHz, 6.5MHz		20		ns		
CLG_SD	Chroma Luma Gain	f=3.58MHz ref to SD in at 400kHz	95	100	105	%		
CLD_SD	Chroma Luma Delay	f=3.58MHz ref to SD in at 400kHz		5.5		ns		

Notes: SNR=20 · log (714mV / rms noise).

Specifications subject to change without notice.



Typical Application



Notes:

1. In some applications, if the power supply noise needs to be filtered, the ferrite bead is recommended in a value of $600\Omega@100MHz$, instead of RC network. RC network normally will lower the power supply resulting in the degraded the audio performance. If the resistor is not chosen properly, which can trigger the internal UVP detection circuit and mute the output. As depicted below.

2. In order to protect the device against the power surge, transient voltage suppressor (TVS) devices are recommended at the output pins OUTL/OUTR.





Typical Performance Characteristics

At T_A = +25°C, C_{PUMP} =0.33µF, C_{PVSS} =0.33µF,unless otherwise noted.





Application Notes

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Gain-SettingResistorsRanges and Input-Blocking Capacitors

The gain-setting resistors, R_{IN} and R_{FB} , must be chosen so that noise, stability, and input capacitor size of the Audio line driver are kept within acceptable limits. Voltage gain is defined as R_{FB} divided by R_{IN} .

Table 1 lists the recommended resistor value for different gain settings. Selecting values that are too low demands a large input ac-coupling capacitor C_{IN} . Selecting values that are too high increases the noise of the amplifier.

The gain-setting resistor must be placed close to the input pins to minimize capacitive loading on these input pins and to ensure maximum stability.

Input Res.,	Feedback Res.,	Inverting Gain
R _{IN}	R _{fb}	
22 kΩ	22 kΩ	-1 V/V
15 kΩ	30 kΩ	-2 V/V
10 kΩ	100 kΩ	-10 V/V

Table 1 Input Capacitor with 2Hz cutoff and Resistor Values Recommended

$$f_{CIN} = \frac{1}{2\pi R_{IN}C_{IN}}, \quad \text{or} \quad$$

 $C_{IN} = \frac{1}{2\pi R_{IN} f_{CIN}}$

Equation 1 Cutoff decision Cutoff



Figure 2 Inverting Gain Configurations

INPUT-BLOCKING CAPACITORS

DC input-blocking capacitors are required to be added in series with the audio signal into the input pins of the Audio line driver. These capacitors block the dc portion of the audio source and allow the Audio line driver inputs to be properly biased to provide maximum performance.

These capacitors form a high-pass filter with the input resistor, R_{IN} . The cutoff frequency is calculated using the equation below. For this calculation, the capacitance used is the input-blocking capacitor, and the resistance is the input resistor chosen from Table 1; then the frequency and/or capacitance can be determined when one of the two values is given.

2nd Order Filter Typical Application

Several audio DACs used today require an external low-pass filter to remove out-of-band noise. This is possible with the Audio line driver, as it can be used like a standard OPAMP. Several filter topologies can be implemented, single-ended. In Figure 3, a multi-feedback (MFB) with single-ended input is shown.



An ac-coupling capacitor to remove dc content from the source is shown; it serves to block any dc content from the source and lowers the dc-gain to 1, helping reducing the output dc-offset to minimum.

The resistor values should have a low value for obtaining low noise, but should also have a high enough value to get a small size ac-coupling capacitor.



Figure 5 Second-Order Active Low-Pass Filter

Charge Pump Flying Capacitor and PVSS Capacitor

The charge pump flying capacitor serves to transfer charge during the generation of the negative supply voltage. The PVSS capacitor must be at least equal to the charge pump capacitor in order to allow maximum charge transfer. Low ESR X5or X7 capacitors are recommended selection, and a value of 0.33μ F is typical. Capacitor values that are smaller than 0.33μ F can be used, but the maximum output voltage may be reduced and the device may not operate to specifications.

Decoupling Capacitors

The Audio line driver requires adequate power supply decoupling to ensure that the noise and total harmonic distortion (THD) are low. A good low equivalent-series-resistance (ESR) ceramic capacitor, typically a combine of paralleled 0.1μ F and 10μ F, placed as close as possible to the device VDD lead works best. Placing this decoupling capacitor close to the Audio line driver is important for the performance of the amplifier. For filtering lower-frequency noise signals, a 10μ F or greater capacitor placed near the audio power amplifier would also help, but it is not required in most applications because of the high PSRR of this device.

Pop-Free Power-Up

Pop-free power up is ensured by keeping the EN (shutdown pin) low during power-supply ramp up and ramp down. The EN pin should be kept low until the input ac-coupling capacitors are fully charged before asserting the EN pin high to achieve pop-less power up. Figure 6 illustrates the preferred sequence.





External Under-voltage Detection

External under-voltage detection can be used to shut down the audio line driver before an input device can generate a pop. Although the shutdown voltage is 1.205V typically, customers need to consider the accuracy of system passive components such as resistors and associated temperature variation. Users often select a resistor divider to obtain the power-on and shutdown threshold for the specific application. The typical thresholds can be calculated as follows, respectively for VSUP_MO at 5V and 12V. Usually for best power down noise performance, 12V supply is recommended for UVP circuitry as below. Typically this 12V is the power supply which generates the 5V supply for the audio line driver VDD pins.

Case 1: VSUP_MO= 12V (Recommended)

V_{UVP}=(1.205V-6uA*R13)*(R11+R12)/R12;

V_{hysteresis}=5uA*R13*(R11+R12)/R12;

With the condition R13>>R11//R12.

For example, if R11=11k, R12=1.4k and R13=47k,

Then V_{UVP}=7.334V; V_{hysteresis}=2.081V

Here, V_{UVP} is the shutdown threshold.



In this case, the voltage at UVP pin 12 is greater than 1.44V under worst case of VSUP_MO ripples.

Case 2: VSUP_MO= 5.0V



In this case, the voltage at UVP pin 12 is greater than 1.44V under worst case of VSUP_MO ripples.

Capacitive Load

The Audio line driver has the ability to drive a high capacitive load up to 220pF directly. Higher capacitive loads can be accepted by adding a series resistor of 47Ω or larger.

For further assistance, please contact DIOO worldwide sales office to seek technical support. You can find DIOO sales office information at <u>www.dioo.com</u>.





Symbol	Dimensions In Millimeters				
Symbol	Min	Nom	Max		
A	-	-	1.20		
A1	0.05	-	0.15		
A2	0.90	1.00	1.05		
A3	0.34	0.44	0.54		
b	0.20	-	0.28		
b1	0.20	0.22	0.24		
С	0.10	-	0.19		
c1	0.10	0.13	0.15		
D	4.86	4.96	5.06		
E	6.20	6.40	6.60		
E1	4.30	4.40	4.50		
е		0.65BSC			
L	0.45	0.60	0.75		
L1		1.00REF			
L2		0.25BSC			
R	0.09	-	-		
R1	0.09	-	-		
S	0.20	-	-		
θ1	0 °	-	8°		
θ2	10°	12°	14°		
θ3	10°	12°	14°		



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